

APPLICABILITY TABLE

PRODUCT
LE70-868
NE70-868



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1. Introduction

1.1. Scope

Scope of this document is to present the features and the application of the Telit xE70-868 radio modules.

1.2. xE70-868 Product Description

The xE70-868 module is a multi-channel radio board, delivering up to 500mW in the 868 MHz ISM band (unlicensed frequency band).

It is delivered with preloaded protocol stack:

“x”	Product name	Stack functionality
L	LE70-868	Star Network
N	NE70-868	Low Power Mesh

xE70-868 is pin-to-pin compatible with LE, NE and ME modules working at different frequencies, in particular xE50-868.

xE70-868 is also pin-to-pin compatible with Telit ZE Family (ZigBee 2007 and ZigBee PRO stack).

1.3. Audience

This document is intended for developers using Telit xE70-868 radio modules.

1.4. Contact Information, Support

For general contact, technical support, to report documentation errors and to order manuals, contact Telit Technical Support Center (TTSC) at:

TS-SRD@telit.com
TS-NORTHAMERICA@telit.com
TS-LATINAMERICA@telit.com
TS-APAC@telit.com

Alternatively, use:

<http://www.telit.com/en/products/technical-support-center/contact.php>



2.2. Other Regulatory Requirements

Furthermore, the module complies with the ETSI 300-220-2 v2.4.1 standards (specific for SRD) which main requirements are described in Appendix 1.

Finally, the module complies with the new European Directive 2002/95/EC concerning the Restrictive Usage of Hazardous Substances (RoHS).



3. General Features

3.1. Main Functionalities

The xE70-868 module is a complete solution from serial interface to RF interface. The xE70-868 module has a digital part and a RF part. The radio link is a Half-Duplex bi-directional link.

The digital part has the following functionalities:

- Communication interface
- I/O management
- Micro controller with embedded Telit Software Stack

The RF part has the following functionalities:

- Frequency synthesis
- Front-end
- Low noise reception
- Power amplification
- Packet handling

3.2. Software

The xE70-868 module is provided pre-flashed with Telit in-house stack.

Please refer to Protocol Stack User Guides [7][8] for detailed information.

3.3. Temperature Operating Range

	<i>Minimum</i>	<i>Typical</i>	<i>Maximum</i>	<i>Unit</i>
<i>Operating</i>				
Temperature	- 40	25	+ 85	°C
Relative humidity @ 25°C	20		75	%
<i>Storage</i>				
Temperature	- 40	25	+ 85	°C

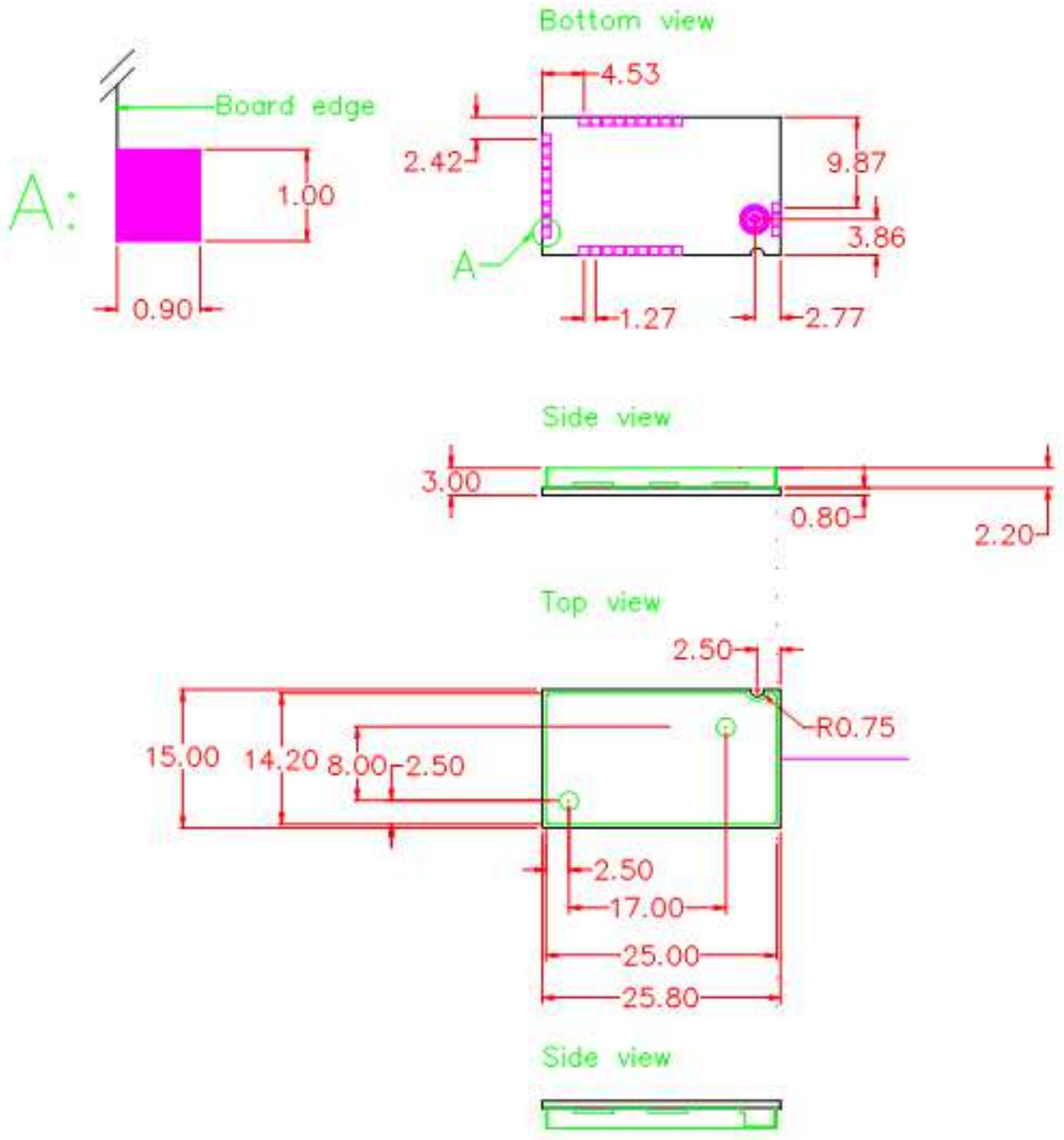


3.4. Mechanical Specifications

<i>Size</i>	Rectangular 25.8 x 15 mm
<i>Height</i>	3 mm
<i>Weight</i>	1.7 g
<i>PCB thickness</i>	0.8 mm
<i>Cover</i>	Mounted above SMD components for EMI reduction and automatic placement <ul style="list-style-type: none"> • Dimensions : 25 x 14.2 x 2.2mm • Thickness : 200µm
<i>Components</i>	All SMD components, on one side of the PCB.
<i>Mounting</i>	Suitable for RoHS reflow process <ul style="list-style-type: none"> • SMD • LGA on the 4 external sides
<i>Number of pins</i>	30



3.4.1. Mechanical drawing



3.5. DC Specifications

Measured on DIP interface with $T = 25^{\circ}\text{C}$, under 50 ohm impedance connected to RF port and default power register setting if nothing else stated.

Max limits apply over the entire operating range, $T = -40^{\circ}\text{C}$ to $+85^{\circ}\text{C}$, $V_{DD} = 2.3\text{V}$ to 3.6V and all channels.

Characteristics xE70-868	Min.	Typ.	Max.	Unit
Power Supply (V_{DD})	+2.3	+3.6	+3.6	V
Consumption at 3.6V				
Maximum output power 500mW (+27dBm)		335	< 400	mA
Reception		25	30	mA
Stand-by (32.768 khz On)		< 2	< 3	μA
I/O low level	GND	-	$0.2 \times V_{DD}$	V
I/O high level	$0.8 \times V_{DD}$	-	V_{DD}	V



3.6. Radio Specifications

Measured on DIP interface at T = 25°C, V_{DD} = 3.6V, 50 ohm impedance and default power register setting if nothing else stated.

ERC Rec 70-03 Frequency Band	Band g3: 869.400 MHz - 869.650 MHz						
RF data rate	1.2 kbps	2.4 kbps	4.8 kbps	9.6 kbps	19.2 kbps	38.4 kbps	57.6 kbps
Numbers of channels	1						
Channel width	250 kHz						
Channel 0	869.525 MHz						
Total Bandwidth	250 kHz						
Transmission							
Duty cycle	≤ 10%						
Modulation Format	2GFSK						
Deviation	± 0.6 kHz	± 1.2 kHz	± 7 kHz	± 7 kHz	± 10 kHz	± 20 kHz	± 30 kHz
Frequency tolerance at 25°C	+/- 2.5 kHz						
RF Output Power at 3.6V	Selectable by software (see Protocol Stack User Guide [7][8]) From +7dBm to +27dBm						
Reception							
Rx filter BW	20 kHz	20 kHz	20 kHz	27 kHz	44 kHz	81 kHz	122 kHz
Sensitivity [dBm] for PER < 0,8 (*)	-119	-119	-117	-115	-113	-110	-108.5

(*) 20 bytes Data Packet not including preamble length



ERC Rec 70-03 Frequency Band	Band g3: 869.400 MHz - 869.650 MHz (channelized operation)
RF data rate	4.8 kbps
Numbers of channels	10
Channel width	25 kHz
Channel 0	869.4125 MHz
Total Bandwidth	250 kHz
Transmission	
Duty cycle	≤ 10%
Modulation Format	2GFSK
Deviation	± 2.4 kHz
Frequency tolerance at 25°C	+/- 2.5 kHz
RF Output Power at 3.6V	Selectable by software (see Protocol Stack User Guides [7][8]) From +7 dBm to +23dBm
Reception	
Rx filter BW	15 kHz
Sensitivity [dBm] for PER < 0,8 Ch1 – Ch5 – Ch10	-116.5 ; -116.5; -116.5



Indian Frequency Band: 865 MHz - 867 MHz				
RF data rate	4.8 kbps	9.6 kbps	19.2 kbps	38.4 kbps
Numbers of channels	10			
Channel width	200 kHz			
Channel 0	865.1 MHz			
Total Bandwidth	2 MHz			
Transmission				
Modulation Format	2GFSK			
Deviation	± 7 kHz	± 7 kHz	± 10 kHz	± 20 kHz
Frequency tolerance at 25°C	+/- 2.5 kHz			
RF Output Power at 3.6V	Selectable by software (see Protocol Stack User Guide [7][8]) From +7 dBm to +27dBm			
Reception				
Rx filter BW	20 kHz	27 kHz	44 kHz	81 kHz
Sensitivity [dBm] for PER < 0,8	-116.5	-114.5	-113	-110



Limits allowed by ETSI standard [1]				
Transmission				
Frequency error	+/- 12.5 kHz @ 25 kHz channelization +/- 87 kHz (+/-100 ppm) > 25 kHz channelization			
ACP for channels ≤ 25 kHz	- 37 dBm in 16 kHz “receiver” filter BW under normal test conditions - 32 dBm in 16 kHz “receiver” filter BW under extreme test conditions			
Modulation bandwidth	Reference Bandwidth (RBW)	Limit	Lower envelope point Minimum frequency	Upper envelope point maximum frequency
	1 kHz	- 30 dBm (1 μW)	$f_{e, lower}$	$f_{e, upper}$
	1 kHz	- 36 dBm (250 nW)	($f_{e, lower} - 200$ kHz)	($f_{e, upper} + 200$ kHz)
	10 kHz	- 36 dBm (250 nW)	($f_{e, lower} - 400$ kHz)	($f_{e, upper} + 400$ kHz)
	100 kHz	- 36 dBm (250 nW)	($f_{e, lower} - 1$ MHz)	($f_{e, upper} + 1$ MHz)
Unwanted emissions in the spurious domain	Frequency	47 MHz to 74 MHz 7,5 MHz to 118 MHz 174 MHz to 230 MHz 470 MHz to 862 MHz	Other frequencies below 1 000 MHz	Frequencies above 1 000 MHz
	State			
	Operating	- 54 dBm (4 nW)	- 36 dBm (250 nW)	- 30 dBm (1 μW)
	Standby	- 57 dBm (2 nW)	- 57 dBm (2 nW)	- 47 dBm (20 nW)
Reception				
Blocking for class 2 equipments	Frequency offset of the unwanted signal	Receiver bandwidth		Minimum offset between wanted and unwanted signals
	+/-2 MHz	15 kHz		≥ 35 dB
		25 kHz		≥ 33 dB
		81 kHz		≥ 28 dB
		122 kHz		≥ 26 dB
	+/-10 MHz	15 kHz		≥ 60 dB
		25 kHz		≥ 58 dB
		81 kHz		≥ 53 dB
122 kHz		≥ 51 dB		
Spurious radiation	Below 1000 MHz		Above 1000 MHz	
	- 57 dBm (2 nW)		- 47 dBm (20 nW)	



3.7. Digital Specifications

Function	Characteristics
μC	<ul style="list-style-type: none"> • 128 kB + 8 kB in system programmable flash • 8 kB RAM • 2 kB E²PROM
Serial link	<ul style="list-style-type: none"> • RS232 TTL Full Duplex • 1200 to 115200 bps • 7 or 8 bits • Parity management • Flow control <ul style="list-style-type: none"> ○ Hardware (RTS/CTS)
Embedded software functionality	<ul style="list-style-type: none"> • Flexibility: <ul style="list-style-type: none"> ○ Pre flashed ○ Customization capability ○ Embedded bootloader for firmware download through serial link or over the air

3.8. Absolute Maximum Ratings

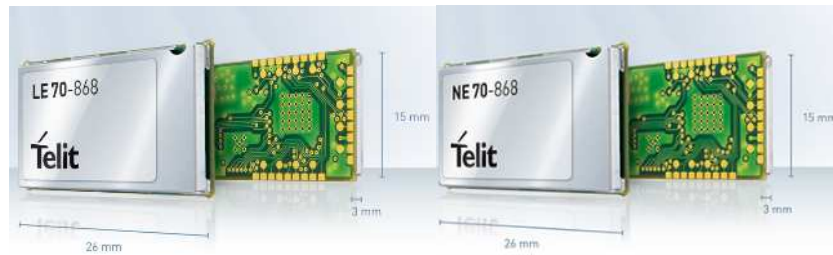
<i>Voltage applied to Vcc, V_{DD} :</i>	-0.3V to +3.6V
<i>Voltage applied to “TTL” Input :</i>	-0.3V to V _{DD} +0.3V



Equipment $x=L$ or N

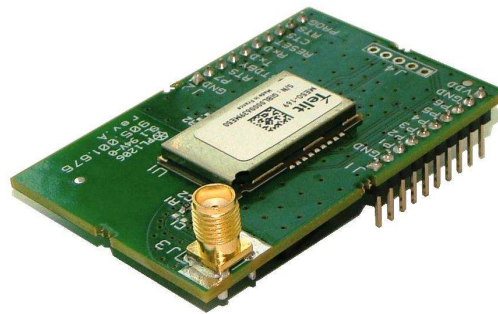
SMD Version

B xE70-868/SMD



DIP Version

B xE70-868/DIP



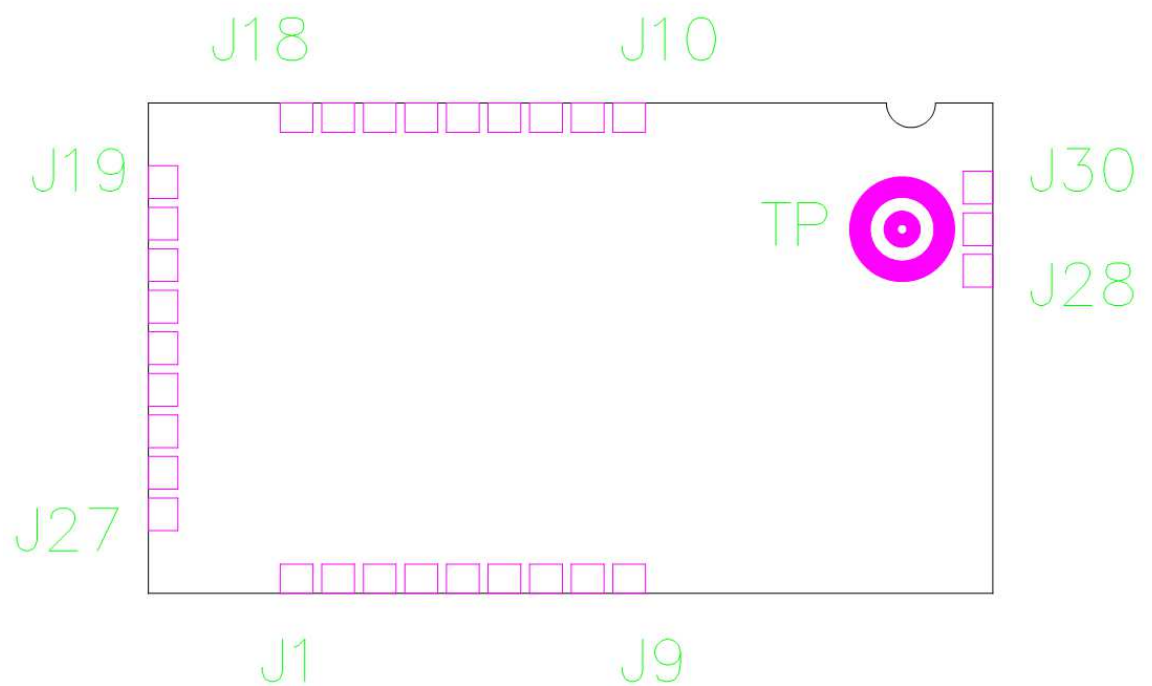
Demo Kit

LE70-868 Demo Kit/NE70-868 Demo Case



4. Pin-out and signals description

4.1. Module Pin OUT (Top View)



CAUTION: reserved pins must not be connected

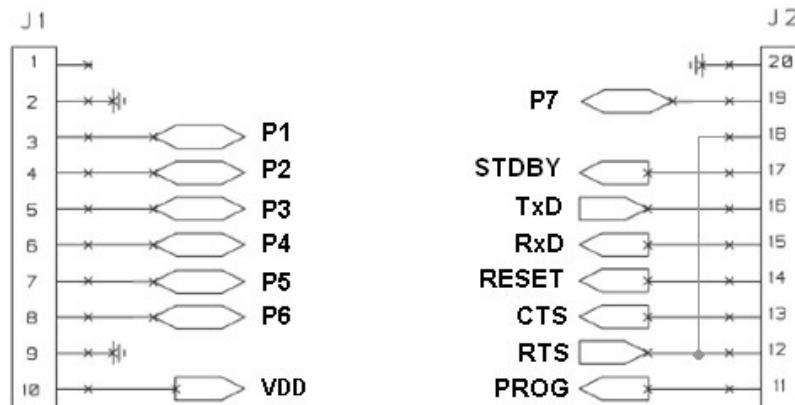
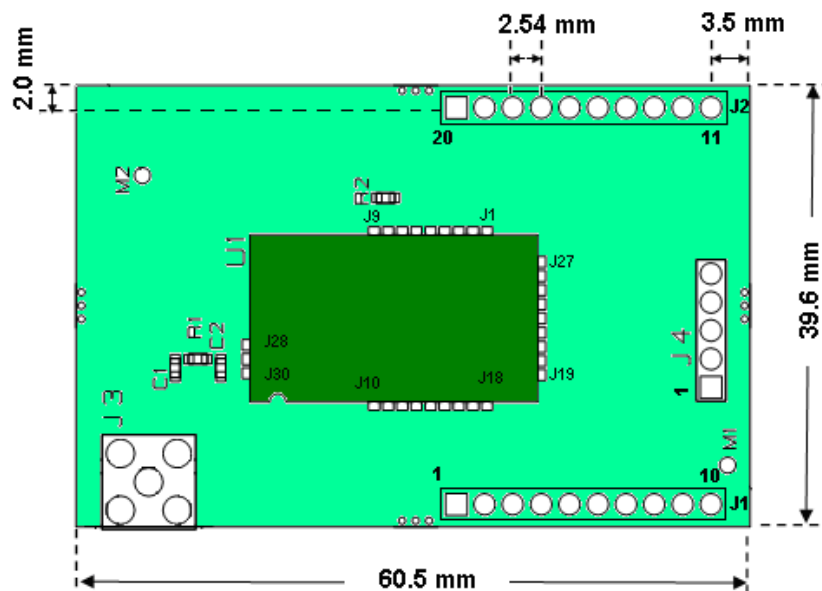


CAUTION: In case you want to use in the same application Telit ZE51 or ZE61 modules J9 and J8 should not be connected, since reserved on these modules (see foot notes on Pin-Out tables).



J3	IO3_A	I/O	analog	Analog Input N°3 (Logic I/O capability)
J2	IO2_P	I/O	TTL	Logic I/O N°2 with interrupt
	RX LED	O	TTL	See reference document [7] Star Network Protocol Stack User Guide
J1	IO1_P	I/O	TTL	Logic I/O N°1 with interrupt
	TX LED	O	TTL	See reference document [7] Star Network Protocol Stack User Guide

4.3. Pin-out of the Module DIP



4.4. Dip-Module Pin-out correspondence table

Pin-Out correspondence between xE70-868/DIP and xE70-868/SMD

xE70-868/DIP			xE70-868/SMD		Comments
Connector	Pin	Name	Pin	Name	
J1	1				Reserved Pin
	2	GND		GND	
	3	P1	J5	IO5_A	
	4	P2	J9	Status TX/RX	
	5	P3	J2	RX LED	
	6	P4	J1	TX LED	
	7	P5	J4	IO4_A	
	8	P6	J3	IO3_A	
	9	GND		GND	
	10	VDD	J25	VDD	
J2	11	PROG	J16	PROG	
	12	RTS	J22	RTS	
	13	CTS	J24	CTS	
	14	RESET	J23	RESET	
	15	RxD	J21	RxD	
	16	TxD	J19	TxD	
	17	STDBY	J18	STAND_BY	
	18	RTS	J22	RTS	
	19	P7	J6	IO6_A	
	20	GND		GND	
J4	1		J14	PDI_DATA	J4 Connector for debugging and flashing
	2		J10	PDI_CLK	
	3		J23	RESET	
	4		J25	VDD	
	5			GND	
			J7	IO7_A	Reserved Pin
			J8	IO8_AD_DA	
J3	SMA connector		J29	Ext_Antenna (Unbalanced RF)	A 50 Ohm coplanar wave guide and a 0 ohm resistor are used to connect J29 to J3



5.2. Storage

The optimal storage environment for XE70-868 modules should be dust free, dry and the temperature should be included between -40°C and +85°C.

5.3. Moisture sensibility

The level of moisture sensibility of the Product is “3” according with standard IPC/JEDEC JSTD-020, take care of all the relative requirements for using this kind of components.

Moreover, the customer has to take care of the following conditions:

- a) The shelf life of the Product inside of the dry bag must be 12 months from the bag seal date,
- b) when stored in a non-condensing atmospheric environment of $\leq 30^{\circ}\text{C} / 60\% \text{ RH}$ according to IPC/JEDEC J-STD-033A paragraph 5
- c) The maximum time between the opening of the sealed bag and the reflow process must be 168 hours if condition b) “IPC/JEDEC J-STD-033A paragraph 5.2” is respected
- d) Baking is required if conditions b) or c) are not respected
- e) Baking is required if the humidity indicator inside the bag indicates 10% RH or more.

5.4. Additional Precautions

Also, it must be noted that due to some components, XE70-868 modules are ESD sensitive device. Therefore, ESD handling precautions should be carefully observed.

5.5. Soldering pad pattern

The surface finished on the printed circuit board pads should be made of Nickel/Gold surface.

The recommended soldering pad layout on the host board for the xE70-868 module is shown in the diagram below:



5.9. Placement

The xE70-868 module can be automatically placed on host boards by pick-and-place machines like any integrated circuit.

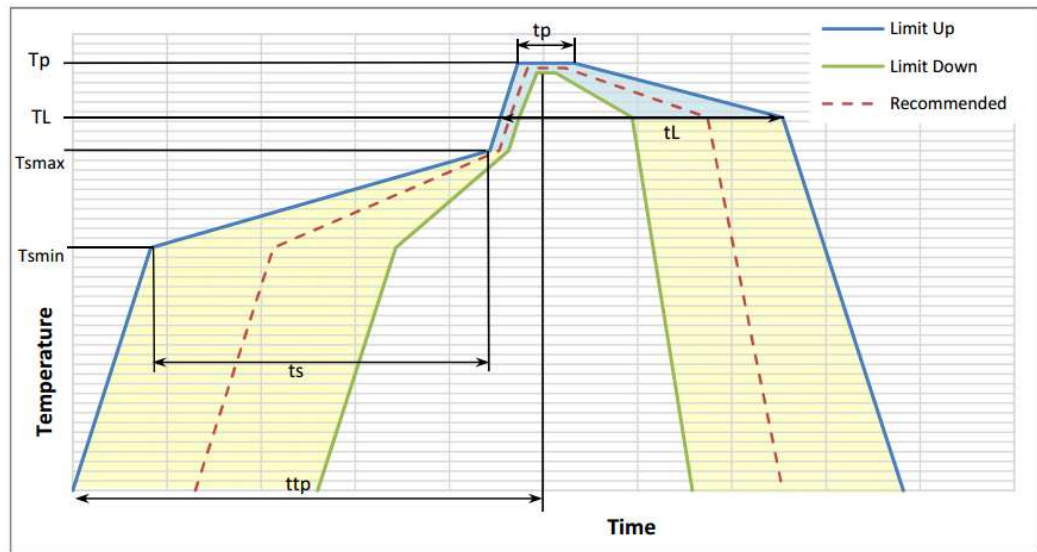
5.10. Soldering Profile (RoHS Process)

It must be noted that xE70-868 module should not be allowed to be hanging upside down during the reflow operation. This means that the module has to be assembled on the side of the printed circuit board that is soldered last.

The recommendation for lead-free solder reflow in IPC/JEDEC J-STD-020D Standard should be followed.

Profile Feature	Pb-Free Assembly
Average ramp-up rate (TL to Tp)	3°C/second max
Preheat	
- Temperature Min (T _{min})	150°C
- Temperature Max (T _{max})	200°C
- Time (T _{min} to T _{max}) t _s	60-180 seconds
T _{max} to TL	
- Ramp-up rate	3°C/second max
Time maintained above:	
- Temperature (TL)	217°C
- Time (tL)	60-150 seconds
Peak Temperature (Tp)	245°C +0/-5 °C
Time within 5°C of actual Peak Temperature (tp)	10-30 seconds
Ramp-down Rate	6°C/second max
Time 25°C to Peak Temperature Tp (ttp)	8 minutes max





The barcode label located on the module shield is able to withstand the reflow temperature.



CAUTION - It must also be noted that if the host board is submitted to a wave soldering after the reflow operation, a solder mask must be used in order to protect the xE70-868 radio module's metal shield from being in contact with the solder wave.



6. Board Mounting Recommendation

6.1. Electrical environment

The best performances of the xE70-868 module are obtained in a “noise free” environment. Some basic recommendations must be followed:

- Noisy electronic components (serial RS232, DC-DC Converter, Display, Ram, bus,...) must be placed as far as possible from the xE70-868 module.

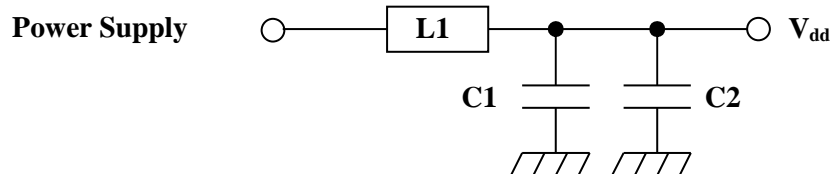


CAUTION – A particular attention must be put on power supply DC-DC converter, due to switching frequency that generates spurious into the receiver band. It can strongly decrease module performances. It is then recommended to put a metallic shield covering DC conversion function.

- Switching components circuits (especially RS-232/TTL interface circuit power supply) must be decoupled with a 100 μ F low ESR tantalum capacitor. And the decoupling capacitor must be as close as possible to the noisy chip.

6.2. Power supply decoupling on xE70-868 module

The power supply of xE70-868 module must be nearby decoupled. A LC filter is strongly recommended in case of DC-DC conversion. It must be placed as close as possible to the radio module power supply pin, VDD.



For example:

Symbols	Reference	Value	Manufacturer
L1	LQH32CN1R0M33	1 μ H	Murata
C1	GRM31CF51A226ZE01	22 μ F	Murata
C2	Ceramic CMS 25V	100nF	Multiple

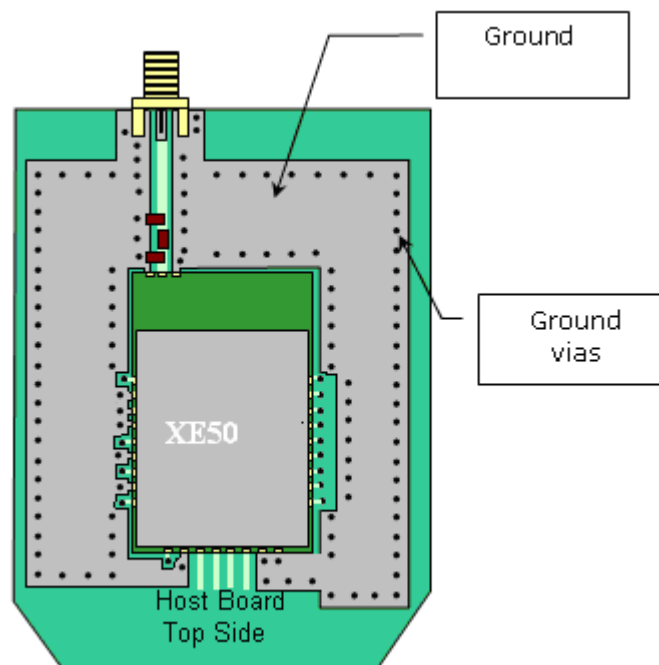
L1 must be chosen carefully with very low serial resistance (ESR) in order to limit voltage drop.



6.3. RF layout considerations

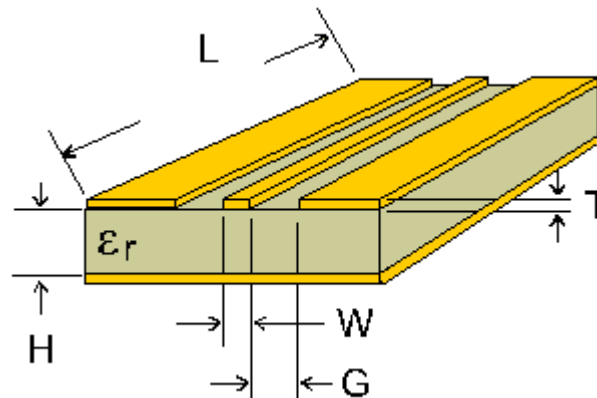
Basic recommendations must be followed to achieve a good RF layout:

- It is recommended to fill all unused PCB area around the module with ground plane
- The radio module ground pin must be connected to solid ground plane.
- If the ground plane is on the bottom side, a via (Metal hole) must be used in front of each ground pad. Especially J28 and J30 (RF Gnd) pins should be grounded via several holes to be located right next to the pins thus minimizing inductance and preventing mismatch and losses.



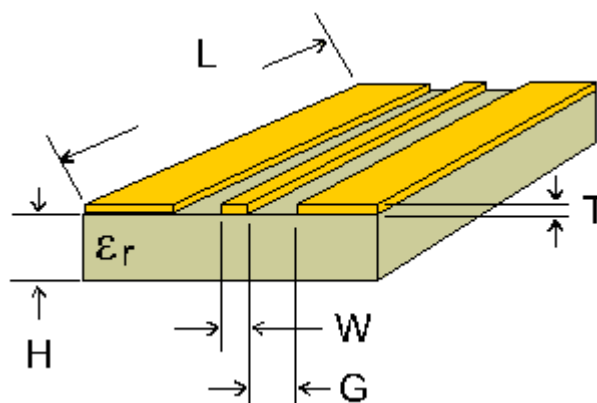
6.4. Antenna connections on printed circuit boards

Special care must be taken when connecting an antenna or a connector to the module. The RF output impedance is 50 ohms, so the strip between the pad and the antenna or connector must be 50 ohms following the tables below. Ground lines should be connected to the ground plane with as many vias as possible, but not too close to the signal line.



PCB material	PCB thickness H (mm)	Coplanar line W (mm)	Coplanar line G (mm)
FR4	0.8	1	0.3
	1.6	1	0.2

Table 1: Values for double face PCB with ground plane around and under coplanar wave guide (recommended)



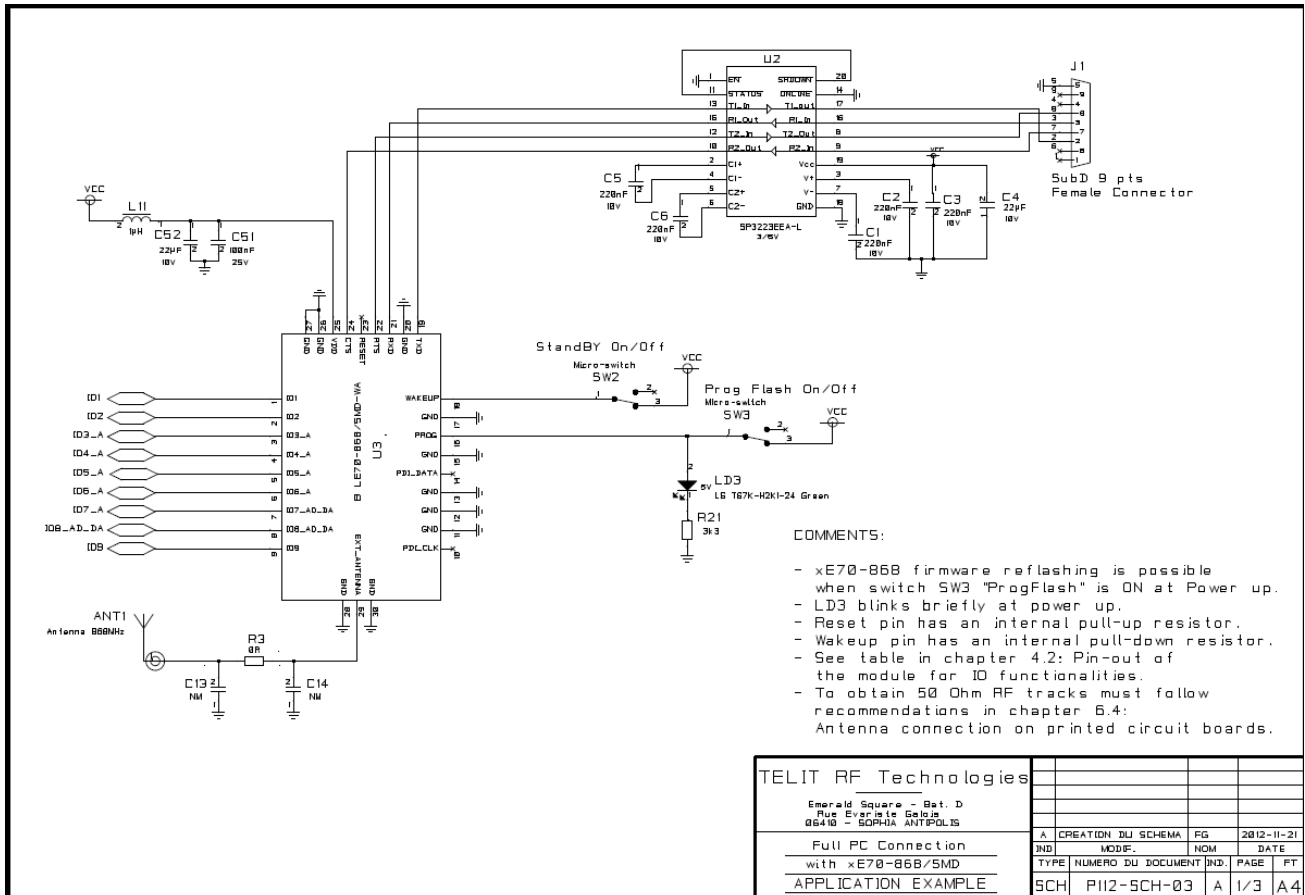
PCB material	PCB thickness H (mm)	Coplanar line W (mm)	Coplanar line G (mm)
FR4	0.8	1	0.22
	1.6	1	0.23

Table 2: Values for simple face PCB with ground plane around coplanar wave guide (not recommended)



6.5. xE70-868 Interfacing

Example of a full RS-232 connection between a PC or an Automat (PLC) and xE70-868



Example of minimum connections for communication between a PC and xE70-868

